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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				Application Number	Not Yet Assigned
				Filing Date	Concurrently Herewith
				First Named Inventor	K.F. Day III et al.
				Art Unit	Not Yet Assigned
				Examiner Name	Not Yet Assigned
Page	1	of	1	Attorney Docket Number	TUC920030193US1

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number- Kind Code (if known)			
WAC		US-5379391	01-03-1995	Belsan et al.	
		US-5426761	06-20-1995	Cord et al.	
		US-5627990	05-06-1997	Cord et al.	
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		US-6230200 B1	05-08-2001	Forecast et al.	
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		US-6345338 B1	02-05-2002	Milillo et al.	
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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS				
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published		T
WAC		Daniel A. Connors, et al., "An Architecture Framework for Introducing Predicated Execution into Embedded Microprocessors", Dept. of Electrical and Computer Eng. and Logic Systems Laboratory, pp. 1-10; 1999		
		Robert C. Elliot, "Information Technology - SCSI Block Commands - 2 (SBC-2)", working draft American National Standard, Project T10/1417-D, Revision 12, 25 January 2004, reference number ISO/IEC 14776-322:200x ANSI INCITS.***:200x, pp. i-iv, 46-48		
↓		IBM Corporation, "PowerPC 601 Microprocessor", White paper, [online] [Retrieved on February 27, 2004], retrieved from the Internet at <URL: <a href="http://www-1.ibm.com/servers/eserver/pseries/hardware/whitepapers/power/ppc_601.html">http://www-1.ibm.com/servers/eserver/pseries/hardware/whitepapers/power/ppc_601.html</a> >		

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